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For: SWITCHED CAPACITOR INTEGRATOR SYSTEM

1 1. A switched capacitor integrator system comprising:
2 an input cascoded amplifier circuit;
3 a summing junction;
4 an integrating switched capacitor circuit connected to the output of said
5 cascoded amplifier circuit and to said summing junction; said integrating switched capacitor
6 circuit including an input switched capacitor circuit responsive to an input and connected to said
7 summing junction; and
8 a correlated double sampling capacitor circuit including an offset capacitor
9 interconnected between said summing junction and said input of said cascoded amplifier circuit.

1 2. The switched capacitor integrator system of claim 1 in which said input switched
2 capacitor circuit includes a variable capacitance circuit.

1 3. The switched capacitor integrator system of claim 2 in which said variable
2 capacitance circuit includes a plurality of capacitors.

1 4. The switched capacitor integrator system of claim 3 in which said variable
2 capacitance circuit includes a plurality of switches at its input.

1 5. The switched capacitor integrator system of claim 3 in which said variable

2 capacitance circuit includes a plurality of switches at its output.

1 6. The switched capacitor integrator system of claim 1 in which said correlated double
2 sampling capacitor circuit includes a hold capacitor interconnected to the output of said cascoded
3 amplifier circuit and through a first switch to the input of said cascoded amplifier circuit and
4 through a second switch to a bias voltage.

1 7. The switched capacitor integrator system of claim 6 in which said hold capacitor is
2 connected to said output of said cascoded amplifier circuit through a third switch.

1 8. The switched capacitor integrator system of claim 1 in which said input switched
2 capacitance circuit includes a gain switching circuit.

1 9. The switched capacitor integrator system of claim 1 in which said summing
2 junction is adapted for receiving additional inputs.

1 10. The switched capacitor integrator system of claim 9 in which said additional inputs
2 include a digital to analog converter (DAC) circuit.

1 11. The switched capacitor integrator system of claim 10 including a centering circuit
2 and said additional inputs include a centering circuit output.

1 12. The switched capacitor integrator system of claim 11 in which said centering

2 circuit includes a center switching circuit responsive to an input source that may be shared with the
3 DAC.

1 13. The switched capacitor integrator system of claim 1 which is a single ended
2 system.

1 14. The switched capacitor integrator system of claim 1 which is a differential system
2 and there is a second complementary switched capacitor circuit and a second complementary
3 correlated double sampling circuit.

1 15. The switched capacitor integrator system of claim 1 in which said inputs include
2 continuous voltage inputs.

1 16. The switched capacitor integrator system of claim 1 in which said cascoded
2 amplifier circuit includes a plurality of stages and at least the first stage input is cascoded.

1 17. The switched capacitor integrator system of claim 11 in which said centering
2 circuit includes a plurality of capacitors and a switching circuit to select a capacitor to provide a
3 predetermined centering voltage.

1 18. The switched capacitor integrator system of claim 16 in which said first stage
2 includes an input differential cascoded transistor pair for reducing Miller capacitance effects from
3 the input to the output of said input differential transistor pair.

1 19 The switched capacitor integrator system of claim 1 in which said cascoded
2 amplified circuit includes a load circuit.

1 20. The switched capacitor integrator system of claim 19 in which said cascoded load
2 circuit includes current sources.

1 21. The switched capacitor integrator system of claim 19 in which said cascoded load
2 circuit includes resistors.

1 22. The switched capacitor integrator system of claim 19 in which said cascoded load
2 circuit includes diode connected MOSFET's.

 23. The switched capacitor integrator system of claim 1 in which said cascoded
amplifier circuit includes a plurality of stages and at least the first stage includes an input
differential cascoded transistor pair for reducing Miller capacitance effects from the input to the
output of said input differential transistor pair.